KAKATIYA UNIVERSITY: WARANGAL
TIME TABLE FOR M.TECH I-Year: I-SEMESTER (REGULAR) EXAMINATION TO BE HELD IN THE MONTH OF FEBRUARY, 2020

CENTRE: KU College of Engineering & Technology, KU Campus **Date: 03-02-2020**

a) Kakatiya Institute of Technology and Science, Warangal
b) Warangal Institute of Technology & Science, Oorugonda, Warangal
c) Chaitanya Institute of Technology & Science, Kishanpura, HNK
d) Vinuthna Institute of Technology & Science, Hasanparthy
e) KU College of Engineering & Technology, KU Campus

TIME: From 2.00 p.m. to 5.00 p.m.

Date & Day	Structural & Construction Engineering (6001)	Design Engineering (6301)	Digital Communication (6601)	Software Engineering (6901)	Computer Science and Engineering (7601)	VLSI & Embedded System Design (7201) and VLSI Systems Design (7401)	CAD/CAM (6401)	Power Electronics (7901)
17-02-2020 (Monday)	Numerical and Statistical Methods	Optimization Methods in Engineering Design	Detection & Estimation Theory	Discrete Mathematics & Optimization Techniques	Data Structures and Algorithms Design	Optimization Techniques & Graph Theory (VLSI & Embedded System Design Only) Advanced Digital Signal Processing (VLSI Systems Design Only)	Optimization Methods in Engineering Design	Machine Modeling & Analysis
19-02-2020 (Wednesday)	Limit Analysis of Concrete Structures	Fundamental Principles of Engineering Design	Data & Computer Communication	Object Oriented Software Engineering	Advanced Software Engineering	Digital Design (Common VLSI & Embedded System Design and VLSI system Design)	Fundamental Principles of Engineering Design	Analysis of Power Electronic Converts
22-02-2020 (Saturday)	Advanced Concrete Technology	Stress Analysis	Advanced Digital Signpal Processing	Software Project Management	Computer Networks and Security	Analog Design (Common VLSI & Embedded System Design and VLSI system Design)	Stress Analysis	Modern Control Theory
24-02-2020 (Monday)	Advanced Analysis of Structures	Mechanical Vibrations	Microwave & Optical Fiber Communication System	Advanced Operating Systems	Data Mining & Data Warehousing	VLSI Technology (Common VLSI & Embedded System Design and VLSI system Design)	Mechanical Vibrations	Power Electronic Controls of DC Drives
26-02-2020 (Wednesday)	Construction Techniques and Equipment	Computer Aided Design & Graphics	Data Compression Techniques	Advanced Computer Architecture	Advanced Computer Architecture	Embedded Systems Concepts (Common VLSI & Embedded System Design and VLSI system Design)	Computer Aided Design & Graphics	Elective – I HVDC Transmissions
28-02-2020 (Friday)	**ELECTIVE** a) Total Quality Management (or) b) Composite constriction Materials	**ELECTIVE** **a)** Smart Structures (DE Only) (or) **b)** Design for Manufacture	**ELECTIVE** Embedded System Design Artificial Neural Network	**ELECTIVE** (a) Data Structures & Algorithms(or) (d) Genetic Algorithms	**Elective** **a)** Advances in Compiler(or) Construction **b)** ADBMS (or) **c)** Advanced Operating Systems	**ELECTIVE** Data Communication & Computer Networks (Common VLSI & Embedded System Design and VLSI system Design)	**ELECTIVE – I (d)** Fault Diagnosis of Machines	**Elective – II** Alternative Sources of Electric Energy

ADDL. CONTROLLER OF EXAMINATIONS **CONTROLLER OF EXAMINATIONS**

Note: - *Any Omission or Clash in the Time-Table may kindly be intimated to the Controller of Examination, K.U., Warangal, immediately.*