



OFFICE OF THE CONTROLLER OF EXAMINATIONS KAKATIYA UNIVERSITY: WARANGAL -506 009 (T.S.)

TIME TABLE FOR M.TECH I-Year: I-SEMESTER (REGULAR) EXAMINATION TO BE HELD IN THE MONTH OF JULY/AUGUST, 2021

No.209/E9/KU/2021

Date: 20-07-2021

TIME TABLE

TIME: From 10.00 a.m. to 1.00 p.m. (COVID- 19)

Date & Day	Structural & Construction Engineering (6001)	Design Engineering (6301)	Digital Communication (6601)	Software Engineering (6901)	Computer Science and Engineering (7601)	VLSI & Embedded System Design (7201) and VLSI Systems Design (7401)	CAD/CAM (6401)	Power Electronics (7901)
26/07/2021 (Monday)	Numerical and Statistical Methods	Optimization Methods in Engineering Design	Detection & Estimation Theory	Discrete Mathematics & Optimization Techniques	Data Structures and Algorithms Design	Optimization Techniques & Graph Theory (VLSI & Embedded System Design Only) Advanced Digital Signal Processing (VLSI Systems Design Only)	Optimization Methods in Engineering Design	Machine Modeling & Analysis
28/07/2021 (Wednesday)	Limit Analysis of Concrete Structures	Fundamental Principles of Engineering Design	Data & Computer Communication	Object Oriented Software Engineering	Advanced Software Engineering	Digital Design (Common VLSI & Embedded System Design and VLSI system Design)	Fundamental Principles of Engineering Design	Analysis of Power Electronic Converts
30/07/2021 (Friday)	Advanced Concrete Technology	Stress Analysis	Advanced Digital Signal Processing	Software Project Management	Computer Networks and Security	Analog Design (Common VLSI & Embedded System Design and VLSI system Design)	Stress Analysis	Modern Control Theory
03/08/2021 (Tuesday)	Advanced Analysis of Structures	Mechanical Vibrations	Microwave & Optical Fiber Communication System	Advanced Operating Systems	Data Mining & Data Warehousing	VLSI Technology (Common VLSI & Embedded System Design and VLSI system Design)	Mechanical Vibrations	Power Electronic Controls of DC Drives
05/08/2021 (Thursday)	Construction Techniques and Equipment	Computer Aided Design & Graphics	Data Compression Techniques	Advanced Computer Architecture	Advanced Computer Architecture	Embedded Systems Concepts (Common VLSI & Embedded System Design and VLSI system Design)	Computer Aided Design & Graphics	Elective – I HVDC Transmissions
07/08/2021 (Saturday)	ELECTIVE a) Total Quality Management (or) b) Composite construction Materials	ELECTIVE a) Smart Structures (DE Only) (or) b) Design for Manufacture	ELECTIVE Embedded System Design Artificial Neural Network	ELECTIVE (a) Data Structures & Algorithms(or) (d) Genetic Algorithms	Elective a) Advances in Compiler(or) Construction b) ADBMS (or) c) Advanced Operating Systems	ELECTIVE Data Communication & Computer Networks (Common VLSI & Embedded System Design and VLSI system Design)	ELECTIVE – I (d) Fault Diagnosis of Machines	Elective – II Alternative Sources of Electric Energy

K.M. Reddy

CONTROLLER OF EXAMINATIONS

Note:- 1. Any Omission or Clash in the Time-Table may kindly be intimated to the Controller of Examination, K.U., Warangal, immediately.

2. The Chief Superintendents' of Exam Centers and the principals' of the colleges concerned are requested to take necessary precautions and preventive safety measures in conducting examination as per the existing COVID – 19 guidelines.