



**OFFICE OF THE CONTROLLER OF EXAMINATIONS KAKATIYA UNIVERSITY: WARANGAL -506 009 (T.S.)**  
**TIME TABLE FOR M.TECH I-Year: I-SEMESTER (REGULAR) EXAMINATION TO BE HELD IN THE MONTH OF JULY/AUGUST, 2021**

**No. 214/E9/KU/2021**

**Date: 22-07-2021**

**Exam Centers:**

1. **Chaitanya Inst.Of Tech.,&Science, Kishanpura, Hanamkonda(493)**
- a) Chaitanya Inst. of Tech., & Science, Kishnapura, Hanamkonda (493)      c) Warangal Inst.of Tech & Science, Oorugonda, Warangal. (474)
- b) KU College of Engg. & Tech, KU Campus, Warangal (567)                      d) Vinuthna Inst.Tech.,&Science, Hasanparthy, Warangal. (473)

**Time: From 02.00 p.m. to 05.00 p.m. (COVID- 19)**

| Date & Day             | Structural & Construction Engineering (6001)   | Design Engineering (6301)  | Digital Communication (6601)   | Software Engineering (6901)   | Computer Science and Engineering (7601)   | VLSI & Embedded System Design (7201) and VLSI Systems Design (7401)  | CAD/CAM (6401)   | Power Electronics (7901)                                       |
|------------------------|--|--|--|---|---|--|--|--|
| 26/07/2021 (Monday)    | Numerical and Statistical Methods  | Optimization Methods in Engineering Design   | Detection & Estimation Theory  | Discrete Mathematics & Optimization Techniques                                    | Data Structures and Algorithms Design   | Optimization Techniques & Graph Theory (VLSI & Embedded System Design Only)<br>Advanced Digital Signal Processing (VLSI Systems Design Only) | Optimization Methods in Engineering Design             | Machine Modeling & Analysis                                    |
| 28/07/2021 (Wednesday) | Limit Analysis of Concrete Structures  | Fundamental Principles of Engineering Design                                       | Data & Computer Communication  | Object Oriented Software Engineering  | Advanced Software Engineering   | Digital Design (Common VLSI & Embedded System Design and VLSI system Design)   | Fundamental Principles of Engineering Design           | Analysis of Power Electronic Converts                          |
| 30/07/2021 (Friday)    | Advanced Concrete Technology   | Stress Analysis  | Advanced Digital Signal Processing                                     | Software Project Management   | Computer Networks and Security  | Analog Design (Common VLSI & Embedded System Design and VLSI system Design)  | Stress Analysis  | Modern Control Theory  |
| 03/08/2021 (Tuesday)   | Advanced Analysis of Structures  | Mechanical Vibrations  | Microwave & Optical Fiber Communication System                         | Advanced Operating Systems  | Data Mining & Data Warehousing  | VLSI Technology (Common VLSI & Embedded System Design and VLSI system Design)  | Mechanical Vibrations                                  | Power Electronic Controls of DC Drives                         |
| 05/08/2021 (Thursday)  | Construction Techniques and Equipment  | Computer Aided Design & Graphics   | Data Compression Techniques  | Advanced Computer Architecture  | Advanced Computer Architecture  | Embedded Systems Concepts (Common VLSI & Embedded System Design and VLSI system Design)  | Computer Aided Design & Graphics                       | Elective – I HVDC Transmissions                                |
| 07/08/2021 (Saturday)  | <b>ELECTIVE</b><br>a) Total Quality Management (or)<br>b) Composite construction Materials | <b>ELECTIVE</b><br>a) Smart Structures (DE Only) (or)<br>b) Design for Manufacture | <b>ELECTIVE</b><br>Embedded System Design<br>Artificial Neural Network | <b>ELECTIVE</b><br>(a) Data Structures & Algorithms(or)<br>(d) Genetic Algorithms | <b>Elective</b><br>a) Advances in Compiler(or) Construction<br>b) ADBMS (or)<br>c) Advanced Operating Systems | <b>ELECTIVE</b><br>Data Communication & Computer Networks (Common VLSI & Embedded System Design and VLSI system Design)                      | <b>ELECTIVE – I (d)</b><br>Fault Diagnosis of Machines | <b>Elective – II</b><br>Alternative Sources of Electric Energy |

**Note:- 1.** Any Omission or Clash in the Time-Table may kindly be intimated to the Controller of Examination, K.U., Warangal, immediately.

**2.** The Chief Superintendents' of Exam Centers and the principals' of the colleges concerned are requested to take necessary precautions and preventive safety measures in conducting examination as per the existing COVID – 19 guidelines.

  
**CONTROLLER OF EXAMINATIONS**